

PENDING CLAIMS AS AMENDED

Please amend the claims as follows:

1. Canceled.
2. Canceled.
3. Canceled.
4. Canceled.
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18. Canceled.
19. Canceled.
20. Canceled.
21. (Currently Amended) A method comprising:

configuring a front end voice processor to one of a plurality of configurations, each configuration governing a processing of voice features in accordance with at least one back end voice processor design for recognizing speech based on the voice features.

22. (Previously Added) A method in accordance with claim 21, wherein the configuring comprises configuring the front end voice processor in accordance with a configuration file.

23. (Previously Added) A method in accordance with claim 22, further comprising:
receiving the configuration file through a communication link.

24. (Previously Added) A method in accordance with claim 23, wherein the communication link is a wireless communication link.

25. (Previously Added) A method in accordance with claim 22, further comprising:
determining a current back end design of a back end voice processor in communication with the front end voice processor; and
generating the configuration file corresponding to the current back end design.

26. (Previously Added) A method in accordance with claim 22, wherein the configuring comprises:
adjusting at least one adjustable parameter of at least one block of a plurality of blocks comprising the front end voice processor.

27. (Currently Amended) A method in accordance with claim 26, wherein the at least one block is selected from the group consisting of:

a DC blocking filter block, a noise suppression block, a FIR filtering on waveform block, a pre-emphasis block, a band energy computation block, a critical band partition block, a ~~critical~~ critical band weighting block, a FIR filtering of spectrum block, an IIR filtering of log spectrum block, a DCT/PCT/CT/LDA block, and a combining block.

28. (Previously Added) A method in accordance with claim 21, wherein the configuring comprises programming a digital signal processor (DSP) to perform functions of the front end voice processor.

29. (Previously Added) A method in accordance with claim 21, further comprising:
processing voice features of a voice sample in accordance with a first configuration corresponding to a first back end voice processor design; and
processing voice features of another voice sample in accordance with a second configuration corresponding to a second back end voice processor design, the configuring comprising changing from the first configuration to the second configuration.

30. (Currently Amended) A digital signal processor (DSP) for operating within a voice recognition system and programmed to perform functions of a front end voice processor, the digital signal processor comprising:
a plurality of programmable blocks for performing the functions of the front end voice processor, each of the plurality of programmable blocks having at least one adjustable parameter providing a mechanism for configuring the front end voice processor to one of a plurality of configurations, each configuration governing a processing of voice features in accordance with at least one back end voice processor design for recognizing speech based on the voice features.

31. (Previously Added) A digital signal processor in accordance with claim 30, wherein the configuring comprises configuring the front end voice processor in accordance with a configuration file.

32. (Previously Added) A digital signal processor in accordance with claim 31, wherein the configuration file is received through a communication link.

33. (Previously Added) A digital signal processor in accordance with claim 32, wherein the communication link is a wireless communication link.

34. (Previously Added) A digital signal processor in accordance with claim 30, wherein at least one block of the plurality of programmable blocks is selected from the group consisting of:

a DC blocking filter block, a noise suppression block, a FIR filtering on waveform block, a pre-emphasis block, a band energy computation block, a critical band partition block, a critical band weighting block, a FIR filtering of spectrum block, an IIR filtering of log spectrum block, a DCT/PCT/ICT/LDA block, and a combining block.

35. (Currently Amended) A voice recognition system comprising:

a front end voice processor comprising a plurality of programmable blocks for performing voice processing functions, each of the plurality of programmable blocks having at least one adjustable parameter providing a mechanism for configuring the front end voice processor to one of a plurality of configurations, each configuration governing a processing of voice features in accordance with at least one back end voice processor design for recognizing speech based on the voice features; and

a current back end voice processor for recognizing words from processed voice features received from the front end voice processor, the processed voice features processed in accordance with a configuration file corresponding to the current back end voice processor.

36. (Previously Added) A voice recognition system in accordance with claim 35, wherein the configuration file is received through a communication link between the front end voice processor and the current back end voice processor.

37. (Previously Added) A voice recognition system in accordance with claim 36, wherein the communication link is a wireless communication link.

38. (Previously Added) A voice recognition system in accordance with claim 37, wherein at least one block of the plurality of programmable blocks is selected from the group consisting of:

a DC blocking filter block, a noise suppression block, a FIR filtering on waveform block, a pre-emphasis block, a band energy computation block, a critical band partition block, a critical band weighting block, a FIR filtering of spectrum block, an IIR filtering of log spectrum block, a DCT/PCT/ICT/LDA block, and a combining block.

39. (Currently Amended) A method performed in a voice recognition system, the method comprising:

determining a design of a current back end voice processor in communication with a front end voice processor configurable to processes voice features in accordance with a plurality of configurations, each configuration governing a processing of voice features in accordance with at least one back end voice processor design for recognizing speech based on the voice features; and

configuring the front end voice processor in accordance with a configuration file corresponding to a design of the current back end voice processor.

40. (Previously Added) A method in accordance with claim 39, wherein the determining the design of the current back end voice processor comprises receiving the configuration file through a communication link between the current back end processor and the front end processor.

41. (Previously Added) A method in accordance with claim 40, wherein the communication link is a wireless communication link.

42. (Previously Added) A method in accordance with claim 39, wherein the configuring comprises:

adjusting at least one adjustable parameter of at least one block of a plurality of blocks comprising the front end voice processor.

43. (Previously Added) A method in accordance with claim 42, wherein at least one block of the plurality of programmable blocks is selected from the group consisting of:

a DC blocking filter block, a noise suppression block, a FIR filtering on waveform block, a pre-emphasis block, a band energy computation block, a critical band partition block, a critical band weighting block, a FIR filtering of spectrum block, an IIR filtering of log spectrum block, a DCT/PCT/ICT/LDA block, and a combining block.